



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Janardhanan S. AJIT

Appl. No.: 10/700,443

Filed: November 5, 2003

For: **Methods And Systems For Sensing
And Compensating For Process,
Voltage, Temperature, And Load
Variations**

Confirmation No.: 6402

Art Unit: 2819

Examiner: To Be Assigned

Atty. Docket: 1875.1390002

Information Disclosure Statement

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Listed on accompanying Form PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Applicant has listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicant reserves the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicant has checked the appropriate boxes below.

- 1. Statement under 37 C.F.R. 1.704(d). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this information disclosure statement.
- 2. Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits. No statement or fee is required.
- 3. Filing under 37 C.F.R. § 1.97(c). This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection, or Notice of Allowance, or an action that otherwise closes prosecution in the application.
 - a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart

foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- c. Attached is our PTO-2038 Credit Card Payment Form in the amount of \$ _____ in payment of the fee under 37 C.F.R. § 1.17(p).
4. Filing under 37 C.F.R. § 1.97(d) This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. Enclosed find our PTO-2038 Credit Card Payment Form in the amount of \$ _____ in payment of the fee under 37 C.F.R. § 1.17(p); in addition:
- a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
5. Documents AA1, AB1, AE1, AG1, AH1, AK1, AP1, AQ1, and AR1 were cited in a search report by a foreign patent office in a counterpart foreign application. A copy of the search report, listed as document AO1, was submitted to the Examiner in a parent application (see box 8 below).
6. A concise explanation of the relevance of the non-English language document(s) appears below:
7. Copies of the documents are submitted herewith.
8. Copies of the documents were cited by or submitted to the Patent Office in an IDS that complies with 37 C.F.R. § 1.98(a)-(c) in Application No. 10/178,272, filed June 25, 2002 (now U.S. Pat. No. 6,670,821), which is relied upon for an earlier filing date under 35 U.S.C. § 120. Thus, copies of these documents are not attached. 37 C.F.R. § 1.98(d).

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.


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ATTY. DOCKET NO.
1875.1390002APPLICATION NO.
10/700,443FIRST NAMED INVENTOR
Janardhanan S. AJITFILING DATE
November 5, 2003ART UNIT
2819**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1	2001/0026178 A1	10/2001	Itoh <i>et al.</i>			
	AB1	5,719,509	02/1998	Chan			
	AC1	5,877,647	03/1999	Vajapey <i>et al.</i>			
	AD1	6,060,907	05/2000	Vishwanthaiah <i>et al.</i>			
	AE1	6,137,322	10/2000	Ten Eyck			
	AF1	6,144,218	11/2000	Smith <i>et al.</i>			
	AG1	6,177,819 B1	01/2001	Nguyen			
	AH1	6,184,730 B1	02/2001	Kwong <i>et al.</i>			
	AI1	6,552,583 B1	04/2003	Kwong			
	AJ1	6,583,644 B2	06/2003	Shin			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AK1	DE 40 18 754 A1	12/1991	DE			No
	AL						Yes
	AM						No
	AN						Yes
							No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AO	1	European Search Report from European Patent Application No. 03250012.6, 4 pages, mailed March 26, 2003.
	AP	1	Choy, C.S. <i>et al.</i> , "A Low Power-Noise Output Driver with an Adaptive Characteristic Applicable to a Wide Range of Loading Conditions," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, No. 6, pp. 913-917 (June 1997).
	AQ	1	Huchzermeier, J., "Sauber und schnell schalten Dynamic Output Control ermöglicht verbesserte Schalteigenschaften bei hohen Geschwindigkeiten," <i>Electronik</i> , Vol. 48, No. 20, pp. 50-54, 56 and 58 (October 5, 1999).
	AR	1	CMOS Integrated Circuits, B-Series, Solid State Scientific, Inc., pp. 16-17 (November 1976).

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.